

```

; submit to generate ASM86.COM on a
; DIGITAL MICROSYSTEMS DCS-1
; FEB 1 1982
vax %a\set def [source.cpm86.cpm11.asm86]\
vax brexp.r.plm %fsanr
vax cm.plm %fsanr
vax cm2.plm %fsanr
vax cmac1.plm %fsanr
vax cmac2.plm %fsanr
vax cmac3.plm %fsanr
vax cmac4.plm %fsanr
vax cmac5.plm %fsanr
vax cmsubr.plm %fsanr
vax dline.plm %fsanr
vax ermod.plm %fsanr
vax expr.plm %fsanr
vax files.plm %fsanr
vax global.plm %fsanr
vax instr.plm %fsanr
vax io.plm %fsanr
vax mainp.plm %fsanr
vax anem1.plm %fsanr
vax mnm2.plm %fsanr
vax anem3.plm %fsanr
vax mnm4.plm %fsanr
vax outp.plm %fsanr
vax predef.plm %fsanr
vax print.plm %fsanr
vax pseud1.plm %fsanr
vax pseud2.plm %fsanr
vax scan.plm %fsanr
vax subr1.plm %fsanr
vax subr2.plm %fsanr
vax symb.plm %fsanr
vax text.plm %fsanr
;
vax cm.lit %fsanr
vax cmac.lit %fsanr
vax cmacd.lit %fsanr
vax dev.lit %fsanr
vax equals.lit %fsanr
vax ermod.lit %fsanr
vax macro.lit %fsanr
vax mnm.lit %fsanr
vax outp.lit %fsanr
vax struc.lit %fsanr
;
vax cm.ext %fsanr
vax cm2.ext %fsanr
vax cmlink.ext %fsanr
vax cmsubr.ext %fsanr
vax dline.ext %fsanr
vax ermod.ext %fsanr
vax exglob.ext %fsanr
vax expr.ext %fsanr
vax files.ext %fsanr
vax global.ext %fsanr
vax instr.ext %fsanr
vax io.ext %fsanr
vax mglob.ext %fsanr

```

CP/M-86 v.1.1 (Vol. IV)

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SER. # C81-162

```

vax anem.ext $$fsanr
vax outp.ext $$fsanr
vax predef.ext $$fsanr
vax print.ext $$fsanr
vax pseud1.ext $$fsanr
vax pseud2.ext $$fsanr
vax scan.ext $$fsanr
vax subr1.ext $$fsanr
vax subr2.ext $$fsanr
vax symb.ext $$fsanr
vax text.ext $$fsanr
;
vax brexpr.x86 $$fsanr
vax cmsubr.x86 $$fsanr
vax dline.x86 $$fsanr
vax expr.x86 $$fsanr
vax instr.x86 $$fsanr
vax pseud1.x86 $$fsanr
vax pseud2.x86 $$fsanr
vax symb.x86 $$fsanr
;
vax bnf.tex $$fsanr
;
vax cplnk.asm $$fsanr
vax c86lnk.asm $$fsanr
vax asm86pl.asm $$fsanr
;
vax asm80.sub $$fsanr
vax asm80a.sub $$fsanr
vax asm80b.sub $$fsanr
vax asm80c.sub $$fsanr
sub asm80a

```

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SER. #

etseteof brxor.plm
etseteof cm.plm
etseteof cm2.plm
etseteof cmac1.plm
etseteof cmac2.plm
etseteof cmac3.plm
etseteof cmac4.plm
etseteof cmac5.plm
etseteof cmsubr.plm
etseteof dline.plm
etseteof ermod.plm
etseteof expr.plm
etseteof files.plm
etseteof global.plm
etseteof instr.plm
etseteof io.plm
etseteof mainp.plm
etseteof mnem1.plm
etseteof mnem2.plm
etseteof mnem3.plm
etseteof mnem4.plm
etseteof outp.plm
etseteof predef.plm
etseteof print.plm
etseteof pseud1.plm
etseteof pseud2.plm
etseteof scan.plm
etseteof subr1.plm
etseteof subr2.plm
etseteof symb.plm
etseteof text.plm
;
etseteof cn.lit
etseteof cmac.lit
etseteof caacd.lit
etseteof dev.lit
etseteof equals.lit
etseteof ermod.lit
etseteof macro.lit
etseteof mnem.lit
etseteof outp.lit
etseteof struc.lit
;
etseteof cm.ext
etseteof cm2.ext
etseteof cmlink.ext
etseteof cmsubr.ext
etseteof dline.ext
etseteof ermod.ext
etseteof exglob.ext
etseteof expr.ext
etseteof files.ext
etseteof global.ext
etseteof instr.ext
etseteof io.ext
etseteof mglob.ext
etseteof mnem.ext
etseteof outp.ext
etseteof predef.ext
etseteof print.ext

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SER. # _____

elseteof pseud1.ext
elseteof pseud2.ext
elseteof scan.ext
elseteof subr1.ext
elseteof subr2.ext
elseteof symb.ext
elseteof text.ext
;
elseteof brexpr.x86
elseteof cmsubr.x86
elseteof dline.x86
elseteof expr.x86
elseteof instr.x86
elseteof pseud1.x86
elseteof pseud2.x86
elseteof symb.x86
;
elseteof bnf.tex
;
elseteof cpmlnk.asm
elseteof c86lnk.asm
;
sub asm30b

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SER. =

```

; #1 = PLM80 #2 = ASM80 options
pip bt:=*.lit
pip bt:=*.ext
pip bt:=*.x86
et:isl4
:f1:plm80 mainp.plm debug $1
:f1:plm80 cmac1.plm debug $1
:f1:plm80 cmac2.plm debug $1
:f1:plm80 cmac3.plm debug $1
:f1:plm80 cmac4.plm debug $1
:f1:plm80 cmac5.plm debug $1
:f1:plm80 mnem1.plm debug $1
:f1:plm80 mnem2.plm debug $1
:f1:plm80 mnem3.plm debug $1
:f1:plm80 mnem4.plm debug $1
:f1:plm80 symb.plm debug $1
:f1:plm80 io.plm debug $1
:f1:plm80 subr1.plm debug $1
:f1:plm80 subr2.plm debug $1
:f1:plm80 files.plm debug $1
:f1:plm80 scan.plm debug $1
:f1:plm80 print.plm debug $1
:f1:plm80 predef.plm debug $1
:f1:plm80 ermod.plm debug $1
:f1:plm80 text.plm debug $1
:f1:plm80 outp.plm debug $1
:f1:plm80 expr.plm debug $1
:f1:plm80 brexpr.plm debug $1
:f1:plm80 pseud1.plm debug $1
:f1:plm80 pseud2.plm debug $1
:f1:plm80 cmsubr.plm debug $1
:f1:plm80 instr.plm debug $1
:f1:plm80 dline.plm debug $1
:f1:plm80 global.plm debug $1
:f1:plm80 cm.plm debug $1
:f1:plm80 cm2.plm debug $1
:f1:asm80 cpmInk.asm debug $2
cpm
sub asm30c

```

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```

etis14
:f1:link cmac1.obj,cmac2.obj,cmac3.obj,cmac4.obj,cmac5.obj to f11.mod
:f1:link mnam1.obj,mnam2.obj,mnam3.obj,mnam4.obj,symb.obj to f12.mod
:f1:link io.obj,subr1.obj,subr2.obj,files.obj,scan.obj to f13.mod
:f1:link print.obj,predef.obj,ermod.obj,text.obj,outp.obj to f14.mod
:f1:link expr.obj,brexpr.obj,pseud1.obj,pseud2.obj,cmsubr.obj to f15.mod
:f1:link instr.obj,dline.obj,global.obj,cm.obj,cm2.obj to f16.mod
:f1:link f11.mod,f12.mod,f13.mod to f21.mod
:f1:link f14.mod,f15.mod,f16.mod to f22.mod
:f1:link cpmlnk.obj,mainp.obj,f21.mod,f22.mod,:f0:plm80.lib to asm86.mod
:f1:locate asm86.mod code (103h) map print(asm86.map)
:f1:cpm
e!objcpm asm86
mac asm86pl
era asm86x.com
ren asm86x.com=asm86.com
xsub
sid asm86x.com
f6d80,3000,0
iasm86pl hex
r
g0
save 109 asm86.com

```

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 SER. = _____

```

$ plm86 mainp.plm debug optimize(2) 'p1' xref
$ plm86 cmac1.plm debug optimize(2) 'p1'
$ plm86 cmac2.plm debug optimize(2) 'p1'
$ plm86 cmac3.plm debug optimize(2) 'p1'
$ plm86 cmac4.plm debug optimize(2) 'p1'
$ plm86 cmac5.plm debug optimize(2) 'p1'
$ plm86 anem1.plm debug optimize(2) 'p1'
$ plm86 anem2.plm debug optimize(2) 'p1'
$ plm86 anem3.plm debug optimize(2) 'p1'
$ plm86 anem4.plm debug optimize(2) 'p1'
$ plm86 symb.plm debug optimize(2) 'p1' xref
$ plm86 io.plm debug optimize(2) 'p1' xref
$ plm86 subr1.plm debug optimize(2) 'p1' xref
$ plm86 subr2.plm debug optimize(2) 'p1' xref
$ plm86 files.plm debug optimize(2) 'p1' xref
$ plm86 scan.plm debug optimize(2) 'p1' xref
$ plm86 print.plm debug optimize(2) 'p1' xref
$ plm86 predef.plm debug optimize(2) 'p1' xref
$ plm86 ermod.plm debug optimize(2) 'p1' xref
$ plm86 text.plm debug optimize(2) 'p1' xref
$ plm86 outp.plm debug optimize(2) 'p1' xref
$ plm86 expr.plm debug optimize(2) 'p1' xref
$ plm86 brexpr.plm debug optimize(2) 'p1' xref
$ plm86 pseud1.plm debug optimize(2) 'p1' xref
$ plm86 pseud2.plm debug optimize(2) 'p1' xref
$ plm86 cmsubr.plm debug optimize(2) 'p1' xref
$ plm86 instr.plm debug optimize(2) 'p1' xref
$ plm86 dline.plm debug optimize(2) 'p1' xref
$ plm86 global.plm debug optimize(2) 'p1' xref
$ plm86 cm.plm debug optimize(2) 'p1' xref
$ plm86 cm2.plm debug optimize(2) 'p1' xref
$ asm86 c06lnk.asm debug
$ link86 cmac1.obj,cmac2.obj,cmac3.obj,cmac4.obj,cmac5.obj to f11.mod
$ link86 anem1.obj,anem2.obj,anem3.obj,anem4.obj,symb.obj to f12.mod
$ link86 io.obj,subr1.obj,subr2.obj,files.obj,scan.obj to f13.mod
$ link86 print.obj,predef.obj,ermod.obj,text.obj,outp.obj to f14.mod
$ link86 expr.obj,brexpr.obj,pseud1.obj,pseud2.obj,cmsubr.obj to f15.mod
$ link86 instr.obj,dline.obj,global.obj,cm.obj,cm2.obj to f16.mod
$ link86 f11.mod,f12.mod,f13.mod to f21.mod
$ link86 f14.mod,f15.mod,f16.mod to f22.mod
$ link86 c06lnk.obj,mainp.obj,f21.mod,f22.mod to asm86.mod
$ loc86 asm86.mod nopublics ad(sm(code(0))) od(sm(code,const,stack))
$ h86 asm86
! on micro under cp/m or mp/m
!
! determine BBB and MMM from asm86.mp2 file
! BBB = start of const segment / 16
! MMM = (start of memory segment - start of const segment) / 16 + 100h
!      (4K for symbol table)
!
! gencmd asm86 data[bbb,mmm,xffff]
!   which turns out to be:
! gencmd asm86 data[4ad, a44e, xfff]

```

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SER. # _____

```
* link86 c36lnk.obj,mainp.obj,f21.mod,f22.mod to asm86.mod
* loc86 asm86.mod nopublics od(sm(code(0))) od(sm(code,const,stack))
* h86 asm86
! on micro under cp/m or mp/m
```

```
! determine BBB and MMM from asm86.mp2 file
```

```
! BBB = start of const segment / 16
```

```
! MMM = (start of memory segment - start of const segment) / 16 + 100h
! (4K for symbol table)
```

```
! gencmd asm86 data[bBBB,MMM,xffff]
```

```
! which turns out to be:
```

```
! gencmd asm86 data[4ad, m44e, xfff]
```

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CER = _____